

## F. Matthew Rhodes

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**Summary**      *Senior executive with experience ranging from start-up funding and management to leading a \$1B+ semiconductor business. Track record of leading product development teams and business organizations to profitable, market winning positions in hyper-competitive environments. Focused on innovation and skilled in organization leadership, strategic and financial planning and investor communication.*

**Experience**      *2006-2009      Teranetics, Inc.      Santa Clara, CA*  
**Chief Executive Officer**

Teranetics is a 1<sup>st</sup> tier VC backed bay area start-up developing 10G Ethernet PHYs. Hired by Teranetics as CEO in 2006, joining the start-up team that founded the company 3 years prior.

Raised two rounds of funding totally \$45Million and grew the company to over 70 employees with a remote location in Bangalore.

Secured 1<sup>st</sup> tier design wins for the company's products at target accounts INTEL, Cisco and others, leading Teranetics to clear leadership in the space.

*1997 - 2006      Conexant Systems, Inc.      Newport Beach, CA*  
*(formerly Rockwell Semiconductor Systems)*

**President, Conexant Systems, Inc. 2004-2006**

Planned and executed the merger and integration of Conexant and Globespan/Virata. Managed an organization of over 2400 structured as four business units with General Managers of each reporting to me.

Executed drive to expand development bandwidth through low cost resources in Asia, expanded staff in India from approximately 200 to over 700 and from approximately 30 to over 100 in China in one year.

**President, Broadband Communication Segment 2002 - 2003**

Led the reorganization of the previous Personal Computing and Digital Infotainment Divisions into a single business organization, Broadband Communication Segment.

Managed an organization of over 1500 including Sales, Marketing, Engineering and Business Development. Development Organization included two main locations, San Diego and Newport Beach, and over 10 design centers in US, Europe and Asia.

Drove the revenue of the business segment from \$120M/Quarter to \$170M/Quarter in an eight quarter span. Expanded Gross Margin by over 10% and drove profitability from -10% to +10%; Increased Conexant market value by 500% through the period.

Participated with CEO and CFO in investor communication including quarterly earnings conference call and May 2003 "road show" to launch BCS as independent Conexant upon completion of Mindspeed spin-off in June 2003.

With CEO, conceived and executed the strategy for the merger with Globespan/Virata.

**Sr. Vice President and General Manager, Personal Computing Division Dec. 1998 – 2001**

General Manager of company's largest business unit. Held profitability of the unit high, delivering \$100M profit upside in 1999.

Managed organization of about 500 including Marketing, Engineering, Business Development and Business Planning.

Developed and introduced several new technologies and product platforms in narrowband and broadband DSL access that delivered \$100M's in revenue market share capture of over 50%.

**Vice President of Marketing, Personal Computing Division 1998**

Developed marketing plans and managed marketing staff for approximately \$800M modem business.

**Director of Software Products, Personal Computing Division 1997-1998**

Led Marketing for definition and development of Software Modem Product. Successfully introduced product and garnered over 50% market share and grew product line from \$0 to over \$50M/year run rate in less than 2 years.

**1989 – 1997 Pacific Communication Sciences, Inc. San Diego, CA**

**Director of VLSI Development**

Hired by founding team of Wireless System company to build a chip development capability.

Hired and Organized team for Chip Development growing to over 30 members.

Executed multiple wireless system chip programs including development of first PHS chipset for Japanese market.

Co-inventor of several patents for various wireless systems implementations

**1983 – 1989 MIT Lincoln Laboratory Lincoln, MA**

**Member of Technical Staff**

Researcher in Integrated Circuit Development

Multiple Publications on Wafer Scale Integration Process and Architecture

Developed Patented System for Laser Programmable Gate Arrays.

Ranked in top 10 among the approximately 800 Staff.

**1979-1982 Pennsylvania Power & Light Allentown, PA**

**Mathematician/Programmer**

Software Developer for Electric Utility

**Education**      **1996**      **UCLA – Anderson Business School**      **Los Angeles, CA**  
**Masters of Business Administration**

**1983**      **Lehigh University**      **Bethlehem, PA**

**Master of Science/Electrical Engineering**

- Thesis: Electrical Characteristics of Floating Gate MNOS Devices

**1979**      **Penn State University**      **State College, PA**

**Bachelor of Science/Physics with High Honors**

